DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

INTRODUCTION

Department of Electrical and Electronics Engineering was established in the year 1997. It conducts B.Tech & M.Tech programmes in Electrical and Electronics Engineering. The Department has well- equipped laboratories. It focuses on practical applications in the field of Power Electronics Control Systems & Power Systems.

The Department is accredited by NBA in 2006, 2009 and in 2014. In addition to quality teaching, students are exposed to certification courses and workshops along with personality development programs. Furthermore, required training is imparted to the interested students to prepare for competitive examinations such as GATE, CAT etc. The gradual increase in the number of participants and competitors for various programs and the growing number of placements reflects the impact of these programs.

The department is strengthened with a total of 38 faculties out of which 5 are Doctorates and 4 are pursuing PhD's and 29 are holding M.Tech degree. The department has organized many workshops in collaborations IIT Bombay, IIT Madras, IIT Hyderabad and IIT Kharagpur.

Doctorates in EEE

S.No	Name of the Faculty	Degree Awarded	No. of Publications(journals/conferences)
1	Dr. J. Praveen	2007	80
2	Dr. D. V. Pushpalatha	2012	30
3	Dr. J .Sridevi	2014	13
4	Dr. S. V. Jayaram Kumar	2000	26
5	Dr. D. G. Padhan	2012	22

Research Projects/

FDP's/Seminar Grants/Sanctioned

S.No	Name of the Faculty	Title	Name of the Funding Agency	Amount in Lakhs	Year
1	Dr. J. Praveen	Design of 250kVA Dynamic Voltage Restorer for Power Quality Improvement	Young Scientist, DST, MHRD, Government of India	28.6 Lakhs	2015

2	Dr.D.V.Pushpa Latha	Implementation of Fuzzy Logic Controller for Linear Induction Motor based Magnetic Levitation	Young Scientist, DST, MHRD, Government of India	25.2 Lakhs	2015
3	Dr.D.V.Pushpa Latha	Thermal analysis and management of high power short endurance electrical machines	Women Scientist, INSA, New Delhi..	PED accepted	2015
4	Dr.J.Sridevi	Design and hardware Implementation of five phase Induction motor with five phase supply(Proposed)	Young Scientist, DST, MHRD, Government of India	PED accepted	2015
5	Mr.V.Vijaya Rama Raju	Low Cost indigeneous smart tracker for PV Panels for improved efficiency	MSME Programme,2015, Government of India	8 Lakhs	2015
6	Mr.V.Vijaya Rama Raju	Concentrated Photo Voltaic Solar System	MSME Programme,2015, Government of India	8 Lakhs	2015
7	V.V.S.Madhuri	Solar Simulator	MSME Programme,2014, Government of India	8 Lakhs	2014
8	Mr.V.Vijaya Rama Raju	Computerization and Automation of Electrical Machines Lab	MODROBS	9.2 Lakhs	2009
9	Dr. S.N. Saxena	Seminar Grant	AICTE	1.5lakhs	2009

FACULTY AND THEIR PUBLICATIONS:

Prof P.S.Raju, PROFESSOR, Dept.EEE (ID-197)

Qualifications: M.Tech in Power systems from Andhra University (1968) **B.Tech** (Electrical and Electronics Engineering) (1966).

Experience: 49

Research interest: Power Systems, Power Electronics

Journal Publications/Conference Proceedings: 11

National Journals

1. **Raju P S**, *"Problems in state estimation of power system",* Electrical India, Vol. 15, June 1977.
2. **Raju P S**, *"Load Flow studies on practical power systems",* Electrical and Electronics World, No.1, Vol.4, 1976.
3. **Raju P S**, *"A digital approach to the determination of steady state regime from Park's equations",* Electrical India, Feb 1976.
4. **Raju P S**, *"An arithmetic statement translator", CSI, Jan. 1976.*
5. **Raju P S**, *"Computerization of LT consumer bills and receipts",* Electrical and Electronics World, No.3, Vol.3, 1975.
6. **Raju P S**, "*Data path tracing in a digital processor*", CSI, July 1975
7. **Raju P S**, *"A FORTAN routine for finding the driving point and transfer impedances of a large power system from raw system data",* Electrical India, 15 June 1975.
8. **Raju P S**, "*0ptimum rescheduling of real power generation*", Indian Journal of Power and river valley development, April 1975.
9. **Raju P S**, *"Resynchronization of alternators in regulated power systems",* Indian Journal of Power and river valley development, Jan. 1974
10. **Raju P S**, "*State estimation in power systems*", Indian Journal of Power and river valley development, July 1973.
11. **Raju P S**, "*Influence of prime-mover regulation on the process of resynchronization of turbo alternators*", Indian Journal of The Institution of Engineers (India), Oct. 1972.

Dr. J PRAVEEN, PROFESSOR and HOD of Dept.EEE (ID-1079)

Qualifications: Ph.D from Osmania University in Power Electronics (2007)

M.Tech (2000) Energy Systems from JNTU,Hyderabad, **B.Tech** (Electrical and Electronics Engineering) from Osmania University (1998).

Experience: 19yrs

Research interest: Power Electronics

Journal Publications/Conference Proceedings: 80

International Journals

1. KK Jain, Dr J Praveen," Indian Initiative in Adoption of Smart Grid Technology, A Case Study of Puducherry First Pilot Project", published in Anveshana's International Journal of Research in Engineering and Applied Sciences-AIJREAS, Vol 1, Issue 1, 2016 Jan, pp 9-17, ISSN-2455-6300.
2. Vinay Kumar Awaar, Praveen Jugge, Tara Kalyani S, "Field Test of Cost Effective Voltage Source Inverter For Driving An Induction Motor",published in **IEEE Explorer Digital Library,** IEEE INDICON 2015 1570203957, 978-1-4673-6540-6/15/$31.00 ©2015 IEEE.
3. N.Eashwaramma, J.Praveen , M.Vijaya Kumar, " Modelling and Design of Cascaded 9 Level Voltage Source Converter based DVR for Mitigating the Voltage Sag, Swell, Harmonics, Transients and Flickers in Distributed Power System", published in IOSR Journal of Electrical and Electronics, e-ISSN: 2278-1676,p-ISSN: 2330-3331, Volume 10, Issue 3 Ver.IV (May-Jun.2015), PP 40-49, www.iosrjounals.org
4. N.Eashwaramma, J.Praveen , M.Vijaya Kumar, " Modelling and Simulation of DVR with Multilevel Inverter to Mitigate the Sag, Swell and Harmonics", International Journal of Scientific and Engineering Research, Volume 6, Issue 5, May-2015, ISSN 2229-5518, PP 406-411, www.ijser.org
5. Vinay Kumar. A**, Dr. J Praveen**, Dr. S Tarakalyani "PQ Improvement By Moderation of Multi-Level Inverter Controlling Techniques And Intensifying The Performance of DVR" published in International Journal of Advances in Electrical and Electronic Engineering (AEEE) journal in the month of June 2015 with the **Vol.13 No.2 (2015)** page no.s from 107-114.
6. N. Srinivasa Raoa , Dr. A. Selwin Mich Priyadharsonb , **Dr. J. Praveen**," Simulation of Artificial Intelligent controller based DVR for power quality improvement", published in ELSEVIER International journal of procedia Computer Science 47 (2015) 153– 167, Available online at www.sciencedirect.com.

7. N.Srinivasa Rao, **Dr J.Praveen**, Dr K.Rajan "A New Multifunctional DVR for Compensation of Voltage Sag", published in International Journal of Electrical, Electronics and Telecommunications Engineering ISSN: 2051-3240, Vol.45,2014 Special Issue.1 – pp 526-531

8. DCK Reddy, **J.Praveen**," Kalman Filter Based Unified Power Quality Conditioner For Output Regulation", published in international journal on Advances in Electrical and Electronic Engineering, Vol 4, No.3, 2014, Research India Publications PP 247-252. ISSN 2231-1297

9. K Hussain, **J Praveen**, "A Seven Level Cascaded Multilevel Inverter Based Dynamic Voltage Restorer", Published in International Journal of Electrical, Electronics and Telecommunication Engineering, ISSN: 2051-3240, Vol.44, Issue.1, March 2013, pp. 1121-1129.

10. Dr DP Kothari, **Dr J Praveen**, KK Jain " Adoption of Synchrophasor Technology in Indian Grid Failures & Blackouts", published in the International Journal of International Organization of Scientific Research (IOSR-JECE), ISNB:2278-8735.PP01-10. 2013.

11. K Hussain, **J Praveen**, "Power Quality Improvement, Thd Analysis Using Dstatcom ForLow Voltage System", Published in International Journal of Electrical and Electronics Engineering Research, Volume: 3, Issue: 1, March 31, 2013, pp.175-188.

12. N.Srinivasa Rao, **J.Praveen** "Design and Simulation of New Z Source Inverter for Improving Voltage Quality", published in the International Journal of Electrical and Electronics Engineering Research (IJEEER), Vol.3,Issue 3, Aug 2013, 151-160. ISSN 2250-155X

13. P.N.K.Sreelatha, **J.Praveen,**V.Kamaraju "Voltage Compensation Using D-Statcomunder Unsymmetrical Faults in Distribution Systems with Static Power Converter Fed DcMotor Load" published in International Journal of Engineering Research and development(IJERD) ,e-ISSN: 2278-067X, p-ISSN : 2278-800X, Volume 5, Issue 4 (December 2012),PP. 27-34.
14. K Hussain, **J Praveen**, "Voltage Sag Mitigation Using Distribution Static Compensator System", Published in International Journal of Engineering and Technology, Volume 2, No.5, May, 2012, pp.756-760.
15. P.N.K.Sreelatha, **J.Praveen**, V.Kamaraju "Voltage sags in Distribution Systems with Induction motor loads fed by Power converters and voltage mitigation using DVR and DSTATCOM" published in International Journal of Electrical Engineering (IJEE) journal ISSN0974-2158 Volume 5, Number 7 (2012), pp. 889-901.
16. K Hussain, **Dr J Praveen**," POWER QUALITY ENHANCEMENT USING VSCBASEDDSTATCOM", research paper has been accepted for publication in the International Journal of Engineering and Technology, ISSN 2049 3444, Vol 2, No.1, January ,2012 pp 93-98.
17. **Dr J Praveen**, A Vinay Kumar, " Power Quality Improvement with Dynamic VoltageRestorer using Direct Power Control Strategies" published in International Journal of Electrical, Electronics and Computing Technology pp 20-25 Volume -1, Issue-2, January-April 2011. ISSN 2229-3027.
18. S.Venkateshwarlu, B.P.Muni, A.D.Rajkumar, and **J.Praveen**"Direct Power Control Strategies For Multilevel Inverter Based Custom Power Devices" published in the International Journal of Electrical and Systems Science, and Engineering, Volume 1Number2, Page no. 94-102.Year 2009.
19. S.Venkateshwarlu, B.P.Muni, A.D.Rajkumar, and **J.Praveen**" Virtual Flux DirectPower Control using Space Vector Modulation", accepted for publication in the International Journal of Electrical and Systems. Year 2009.

National Journals

1. Vinay Kumar A, **J.Praveen**, S.Tarakalyani "Various Power Quality Issues: Measurement of Flicker and Mitigation of Voltage Sag", published in the National Journal of Technology, Vol.10, No.3, September, 2014, pp 49 -55. ISSN 0973-1334.
2. Rajender Reddy K, **Dr J Praveen**, M.Sushma "Simulation Study of Indirect Current Control Technique for Shunt Active Filter", published in National Journal on Electrical Power Engineering and Industrial Drives (NJEPEID) Volume-1,Number-2, July-Aug,2012. **ISSN2231-590X** page no's 152-158.

3. K Roopa, **Dr J Praveen**, Dr S Tara Kalyani, "DPFC for Power flow Control in Transmission System", published in National Journal on Electrical Power Engineering and Industrial Drives (NJEPEID) Volume-1,Number-2, July-Aug,2012. **ISSN 2231-590X** pageno's 178-185.
4. A Vinay Kumar, **Dr J Praveen**, Dr S Tara Kalyani, "Adoption of Direct Power Control Strategy for Enhancement of Dynamic Voltage Restorer", published in National Journal on Electrical Power Engineering and Industrial Drives (NJEPEID) Volume-1,Number-2, July-Aug,2012. **ISSN 2231-590X** page no's 200-205.
5. **Dr.J.Praveen**, K.K. Jain, "Electricity-HVDC-Challenges" Published in the proceedingsof 97th Indian Science Congress, 2-7th Jan 2010, **Thiruvananthapuram**, Kerala, India.
6. **Dr.J.Praveen**, K.K. Jain, "Power Quality Issues-Suggested Solutions" Published in theproceedings of 97th Indian Science Congress, 2-7th Jan 2010, **Thiruvananthapuram**, Kerala,India.
7. **Dr. J.Praveen**, K.K.Jain**"**Power generation in India :Vision year 2022 &Expected contribution of Nuclear power and safe its waste disposal methods" paper has beenselected for Poster display during the 98th Indian Science Congress [www.isc2011.in] inSRM University, Kattankulathur, Tamilnadu, January 3 to 7, 2011

Conference Proceedings

National conferences

1. P.Sharat Chandra, J.Praveen, A.Vinay Kumar, " Design and Implementation of Three Phase Inverter Fed Induction Motor", published in the proceedings of National Conference on Innovations and Design Challenges in Electrical and Medical Electronics, 21-22nd , Aug,2015, held at GRIET, Hyderabad. Pp 92-96. ISBN:978-1-944541-82-8.
2. L.Sravan Kumar, A.Vinay Kumar, J.Praveen," Mitigation of Voltage Sag to Improve Power Quality by Dynamic Voltage Restorer", published in the proceedings of National Conference on Innovations and Design Challenges in Electrical and Medical Electronics, 21-22nd , Aug,2015, held at GRIET, Hyderabad. Pp 97-103.ISBN:978-1-944541-82-8.
3. "Indian Initiative in Adoption of Smart Grid Technology, A Case Study of Puducherry First Pilot Project", Mr KK Jain, Dr. Kothari, **Dr J .Praveen**, Published in the Proceedings of National Conference on Innovative Research in Engineering and Technology organized on 25-04-2015 at Dr KV Subba Reddy Institute of Technology, Kurnool, Andhra Pradesh, ISBN : 978-1-63415-634-9, (pp347-355)
4. Kiran Kumar Jain, Dr J Praveen, "Role of Engineers in Development of Telangana State as Energy Surplus Power State Utilizing Modern Technology", published in the proceedings of National Conference on Role of Engineers in the Development of New State of Telangana(NC-REDNeST), organized under TEQIP-II by Chiatanya Bharathi Institute of Technology, Hyderabad(pp 55)
5. Dr.P.Kothari, **Dr. J.Praveen**, K.K.Jain, "Modernization of Indian Grid System by DevelopingUltrafast Measurement System Known as SynchroPhasors Technology Using GPS in PMU's to Avoid System Failure", published in the proceedings of National Conference on Advanced research methodologies in Electrical Engineering(ARMEE-

2013), June 27th& 28th ,2013, Under TEQIP-II, Madanapalle Institute of Technology and Science, Madanapalle, Chittor, Andhra Pradesh. .

6. **Dr.J.Praveen**, "Dynamic Voltage Restorer–Different Topologies" , Published in ISTEsponsored National Conference on Non-conventional Energy Sources, 17th November,2009,Gudlavallur College of Engineering. A.P.
7. Vinaykumar.A,**Dr.J.Praveen**, Dr.S.Tarakalyani "Measurement of Voltage Filcker and Mitigation of Voltage Sag to improvise the power Quality" presented at National Conference on "Power Quality and Industrial Drives" conducted by Sreenidhi Institute of Science and Technology ,Hyderabad,July12-13th 2013.
8. P.N.K.Sreelatha, **J.Praveen**, V.Kamaraju "Improvement of Power quality in11KV/400V Distribution Systems Using D-STATCOM under Open-Conductor and Shortcircuit faults" National Conference On Emerging Trends In Engineering Applications15thMarch, 2012 at Alhabeeb college of Engineering and Technology, Hyderabad.
9. Vinay Kumar, **Dr J Praveen**, Dr S Tara Kalyani, "Adoption of Direct Power ControlStrategy for Enhancement of Dynamic Voltage Restorer" presented at a National Conference on Electrical Power Engineering and Industrial Drives during May 18-19,2012 Organized by Sreenidhi Institute of Science and Technology (Autonomous), Hyderabad.
10. **Dr J Praveen**, "Digital Control System applications in Improvement of Power Quality-Digital Simulation of Dynamic Voltage Restorer", presented at a two day National level workshop on Control Systems applications to Power Electronics Drives and Systems-(CSPEDS-2012) on 31st March-1st April 2012, under TEQIP-II at Malla Reddy Engineering College, Secunderabad.
11. **Dr J Praveen**, "Direct Power Control Strategy in the DVR for enhancing powerquality" presented at a National Conference on Electrical Power Engineering and Industrial drives during May 18-19,2012 Organized by Sreenidhi Institute of Science and Technology(Autonomous), Hyderabad.
12. Rajender Reddy K**, Dr J Praveen**, M.Sushma "Simulation Study of Indirect Current Control Technique for Shunt Active Filter" presented at a National Conference on Electrical Power Engineering and Industrial Drives during May 18-19,2012 Organized by Sreenidhi Institute of Science and Technology (Autonomous), Hyderabad.
13. **Dr J Praveen**, "Power Quality with Dynamic Voltage Restorer using different PWMpatterns", presented at a two day faculty development workshop Feb12&13th, Dept. of Electrical and Electronics Engineering, Malla Reddy Engineering College.
14. **Dr J Praveen**, "Overview on power quality, Simulation and validation of custom power devises", presented at a one day workshop on Recent Trends in Power Systems on 5thJuly, 2011 at, Hi-Tech College of Engineering and Technology, Hyderabad-75.
15. **Dr J Praveen**, "Space vector PWM controlled Dynamic Voltage Restorer to mitigate missing voltage", presented one day workshop on power quality on 16th Sept,2011 at Narshimha Reddy Engineering College MaisammaGuda, Dhualapally, Secunderabad-500014.
16. **DrJ.Praveen**, "Recent trends and advancements in Custom Power Devices andFACTS", presented at one day workshop on Power Quality on 27th Oct, 2011, Department of Electrical and Electronics Engineering, Hyderabad Institute of Technology and

Management(HITAM, Formerly Royal College of Engineering.) Medchal, (R.R. Dist.), Hyderabad-501401.

17. **DrJ.Praveen**, "Overview on Custom Power Devices and Flexible AC transmission systems", presented at Azura 2011, 29th and 30th two day conference Department of Electrical and Electronics Engineering, CMR College of Engineering and Technology, Medchalroad, Hyderabad-501401.
18. **Dr. J.Praveen**, "Advanced technologies in custom power devises and Power Quality",Presented Expert Lecture at a two day state level technical paper contest YATNA-2K10 atDept. of Electrical Engineering, NallaMalla Reddy Engineering College, DivyaNagar,Hyderabad, 26th &27th March,2010.
19. **Dr. J.Praveen**, " Latest trends in custom power devises and Power Quality", Presented Expert Lecture at a one day National level workshop on Advanced Trends in Power System Security & Power Quality ATPSPQ-2010 at Dept. of Electrical Engineering, Vignan Institute of Technology and science, Deshmukhi, Hyderabad,9th July,2010.
20. **Dr. J.Praveen**, "Direct Power Control Strategies for dynamic voltage restore and its application in improving power quality" presented at a two day National Workshop on Recent Advances in Power Systems & Power Electronics, 3rd - 4th September, 2010Sponsored by ISTE & Guru Nanak Engineering College, Hyderabad.
21. **Dr J Praveen**, A Vinay Kumar," Adoption of Direct Power Control Strategy AndDynamic Voltage Restorer for Improvement of Power Quality" presented at sponsored National Conference on Emerging Trends in Engineering & Applications (NCETETA-2010) during 12th & 13th November, 2010 at Shirdi Sai Engineering College, Bengaluru.
22. **Dr J Praveen**, "Emerging trends in monitoring and restoring power quality and design and simulation of Dynamic voltage restorer" presented at a workshop organized at Srinidhi Institute of Science and Technology, 23rd December 2010, Hyderabad.
23. K.K.Jain, **Dr.J. Praveen**, and Dr. Pradeep Nirgude, " Today's Waste Tomorrow's Fuel- Hyderabad to get 50MW from Garbage (MSW) - An Energy Recovery from MSW

    Management" published in the proceedings of National Conference on Power Electronics and Renewable Energy Systems PEARES-2009, March-2009, SSN College of Engineering, Kalavakkam, Chennai.

24. K.K.Jain, **Dr.J. Praveen**, and Dr. PradeepNirgude, "Biogas Production and Bottling into CNG Cylinders and use as Vehicle fuel in India and Abroad(Biogas as Bio CNG)"

    published in the proceedings of National Conference on Power Electronics and Renewable Energy Systems PEARES-2009, March-2009, SSN College of Engineering, Kalavakkam, Chennai, India.

25. K.Hussainaiah, **Dr. J.Praveen**, " Digital Simulation and Analysis of Custom Power Devises to Enhance Power Quality", published in the proceedings of National Conference ofSignal Processing Exceeding Technological Horizon, NCSP-2009, March-2009, MaharastraInstitute of Technology (MIT), Aurangabad, India

26. **Dr.J.Praveen**" Power Quality issues and Improvement with Custom Power Devises" Presented expert lecture at a two day workshop on "Power Quality" at MuffakhamJah

College of Engineering and Technology, Banjara Hills, Hyderabad, 28th and 29th Aug, 2009.

27. **Dr. J.Praveen**, " Power Quality and Power System Security", Presented Expert Lecture at a two day TEQIP-I sponsored workshop on Power System Security" at Dept. of Electrical Engineering, University College of Engineering (Autonomous), Osmania University ,Hyderabad,14th & 15th September,2009.

28. **Dr.J.Praveen**" Mitigation of harmonics, Voltage sag, Voltage flickers and voltage transients using Dynamic Voltage Restorer " Presented expert lecture at one day workshopon "Power Quality" at Aurora's Technological and Research Institute, Hyderabad, 24th Sept,2009.

29. **Dr.J.Praveen**, Dr.H.V.Makthal, Dr.B.P.Muni " Digital Simulation of Dynamic Voltage Restorer to Improve Power Quality" 23rd National Convention of Electrical Engineers & National Seminar on Challenges in Electricity Sector Under Deregulated Environment", November 23-24, 2007, at Institution of Engineers, Pune.

30. **J.Praveen**, B.P.Muni and H.V. Makthal "Simulation of Multilevel Dynamic Voltage RestorerBased on D-Q Parameters", presented at 2nd National Power Electronics Conference, December 22-24, 2005, IIT Kharagpur. Page no.94-97.
31. **J.Praveen**, B.P.Muni and H.V. Makthal "Simulation of Voltage Sag, Disturbances and elimination using Dynamic Voltage Restorer" presented at 2nd National Power ElectronicsConference, December 22-24, 2005, IIT Kharagpur. Page no.103-106.

32. **J.Praveen**, B.P.Muni and H.V. Makthal "Improving Power Quality using Cascaded H-BridgeDynamic Voltage Restorer" presented at Second National Control Instrumentation System Conference CISCON 2005, November 11-12, 2005, MIT, Manipal International Deemed University,Manipal. Page no. 323-327.
33. **J.Praveen**, B.P.Muni and H.V. Makthal "Dynamic Voltage Restorer" presented at NationalConference on Recent Advances in Electrical Engineering, EAR-2005, 10th December, 2005, JNTU College of Engineering ,Anantapur . Page no. 29.
34. **J.Praveen**, B.P.Muni and H.V. Makthal "Simulation of Multilevel Dynamic Voltage Restorer for Power Quality Improvement" presented at Power Transmission Research Interest and Challenges International Seminar and Tutorial 20-22nd December, 2005, Central Power Research Institute,Bangalore. Page no 45.
35. **J.Praveen**, S.Venkateshwarlu, B.P.Muni, A.D.Rajkumar and H.V Makthal "Promising Inverterfor Elimination of Harmonics", AICTE Sponsored National Conference, Control ommunicationsand Information Systems, January 23-24, 2004,Government Engineering College, Goa. Page no.158-163.
36. **J.Praveen**, B.P.Muni ,S.Venkateshwarlu and H.V. Makthal "Review of Dynamic VoltageRestorer for Power Quality Improvement", Industrial Electronics Conference, IECON 2004, IEEE International Conference, Busan, Korea. Page no.749-754.
37. **J.Praveen**, B.P.Muni and H.V. Makthal "Simulation of Dynamic Voltage Restorer based on dq Parameters", National Power Systems Conference NPSC, December 27-30, 2004, IIT Madras,Chennai. Page no. 595-600.
38. **J.Praveen**, S.Venkateshwarlu, B.P.Muni, A.D Rajkumar and H.V.Makthal "Enhancement ofCustom Power Devices Power Quality using Multilevel Inverters",

National Power SystemsConference NPSC, December 27-30, 2004, IIT Madras, Chennai. Page no. 784-789.

39. **J.Praveen**, H.V.Makthal and B.P.Muni "Simulation of Inverter Control Circuit using SABER"Presented at Solid State Switching Devices and Applications: Progress and Prospects, A NationalConference, The Institution of Engineers, 14-16th July, 2002, Hyderabad. Page no 2.15-2.28.

International conferences

1. N.Eashwaramma, **J.Praveen** , M.Vijaya Kumar, " Design and Simulation of Custom Power Device for Power Quality Improvement in Power System", published in the proceedings of 15th IRF International Conference, June 28th, 2015, Bengaluru, India. ISBN: 978-93-85465-45-1, (page no 102-107)
2. N.Srinivasa Rao, Dr A Selwin Mich Priyadharson, **Dr J Praveen**, " Simulation of Artificial Intelligent Controller based DVR for Power Quality Improvement", published in the proceedings of International Conference on Graph Algorithms, High Performance Implementations and Applications (ICGHIA-2014), Jointly organized by Coimbatore Institute of Technology and Berger University College, Norway during, December 17-19th, 2014.(pp 294-308)
3. N Srinivasa Rao, **Dr J.Praveen**, Dr K.Rajan," Power Quality Improvement using Z-Source Dynamic Voltage Restorer", published in the proceeding of International Conference on Innovations in Electrical & Electronics Engineering (ICIEEE-2014), 5-6th,September, 2014, Guru Nanak Institution, Hyderabad. pp 435-439.(ISBN-978-93-82163-55-8).
4. Dr DP Kothari, **Dr J Praveen**, KK Jain " Adoption of Synchrophasor Technology in IndianGrid Failures & Blackouts", presented at Second International conference on Emerging Trendin Engineering, Dr.J.J.Magdum College of Engineering, Jaysingpur, Kolhapur,Maharastra heldon 22nd&23rd Feb,2013
5. S.Venkateshwarlu, B.P.Muni, A.D.Rajkumar, and **J.Praveen**"Direct Power ControlStrategies For Multilevel Inverter Based Custom Power Devices" International Conference on Computer, Electrical and Systems Science, and Engineering, 21-23, May 2008. Published in the proceedings of World Academy of Science, Engineering and Technology, Volume 29,Bankok, Thailand
6. A.Vinaykumar**,Dr.J.Praveen** ,Dr.S.TaraKalyani "Various power Quality issues:Measurement of Flicker and Mitigation of Voltage sag" An International Conference on Intelligent and Efficient Electrical Systems conducted at PSG college of technology Coimbatore, 12-14 Dec 2013
7. P.N.K Sreelatha, **J.Praveen**, V.Kamaraju, 'Effect of Unsymmetrical faults on Distribution lines with Different Line X/R ratios and Voltage Restoration using DVR with Space Vector Control' published in the proceedings of International Conference on Computing, Electronics and Electrical Technologies-ICCEET-2012 held on 21-22 March,2012 organized by Noorul Islam Centre for Higher Education, Kumaracoil,Thuckalay, Tamilnadu. pp.14.
8. P.N.K Sreelatha, **J.Praveen**, V.Kamaraju, 'Voltage Restoration with D-Statcom Under Unsymmetrical Faults on Distribution Lines with variable X/R', published in

International Conference on recent Technology-2012, 9-11, February, Organized by Institute of Knowledge College of Engineering, Pune. pp-E-60.

9. **Dr J Praveen**, "Latest Simulation tools for power electronics and power quality improvement devices design an overview", presented at International Accreditation Councilfor Quality in Education and Research (IACQER) nodal research center on 29th July, 2011 at Swarnandhra College of Engineering and Technology, Narsapur, W.G.Dist. A.P.
10. **Dr J Praveen**, "Fault analysis to mitigate voltage depression using new simulation schemes and design of custom power devices", presented at International Accreditation Council for Quality in Education and Research (IACQER) nodal research center on 30thJuly, 2011 at Raghu Engineering College, Visakhapatnam, A.P.
11. PNK Sreelatha, **Dr J Praveen**, Dr V Kamaraju," Voltage Sag Compensation with DVRfor Different types of faults at a remote place on a remote feeder", accepted for Presentation at Eight Control Instrumentation System **International Conference** ISCON-2011, Modeling and Simulation in Engineering Applications, 3-6th November, Manipal Institute of Technology, Manipal, Karnataka.
12. Vinay Kumar A, **Dr. J.Praveen**," Digital Simulation of Dynamic Voltage Restorer using Direct Power Control Strategy", presented at 2nd International Conference on Nanotechnology and Biosensors (ICNB-2,2011) on 27th and 28th December, 2011 organized by IACQER at Raghu Engineering College, Vishakapatnam, A.P.(page no 302-308).
13. S.Venkateshwarlu, B.P.Muni, A.D.Rajkumar, and **J.Praveen**" Virtual Flux Direct Power Control using Space Vector Modulation" , accepted for presentation at 2nd International Conference of Electrical Engineering Design and Technologies (ICEEDT'08),November 8-10, 2008, Hammamet, Tunisia.
14. **J.Praveen**, B.P.Muni and H.V. Makthal " Simulation of Dynamic Voltage Restorer usingSpace Vector PWM" presented at International Conference on Systemics, Cybernatics andInformatics, ICSCI 2006, January 04-08, 2006 Hyderabad. Page no.188-192 .

Dr. D.V .PUSHPALATHA, PROFESSOR, Dept.EEE (ID-953)

Qualifications: PhD from Andhra University, Vishakhapatnam (2012)

M.Tech Andhra University (1998), **B.Tech** (Electrical and Electronics Engineering) Gitam University (1995).

Experience: 18yrs

Research interest: Control systems

Journal Publications/Conference Proceedings: 30

Journal Publications

International Publications: 10

1. Ananth Babu, Ch.V.N.Raja, **D.V.Pushpalatha**, K.R.Sudha, "Design of Fractional Model Reference Adaptive PID Controller to Magnetic Levitation System with Permagnet", International Journal of Systems, Control and Communications, InderScience Publishers, Vol. 7, No. 1, 2016.

2. Ch.Ravi Kumar, **D.V.Pushpalatha**, K.R.Sudha, "Design of prisoner's dilemma-based fuzzy c-means computed torque controller for PUMA-560 robot manipulator", International Journal of Intelligent Systems Design and Computing, Inderscience Publishers,2015.

3. Ch.Ravi Kumar, **D.V.Pushpalatha**, K.R.Sudha, "Design of Prisoner's Dilemma Based Fuzzy C-Means Computed Torque Controller with Lyapunov Synthesis Linguistic Model for PUMA-560 Robot Manipulator", Journal of Intelligent and Fuzzy Systems, ISSN: print: 1064-1246; under review: 1875-8967; impact factor 0.788, communicated in June, 2014.

4. Ch.Ravi Kumar, **D.V.Pushpalatha**, K.R.Sudha, K.A.Gopala Rao, "Design of Lyapunov based Fuzzy Logic Controller for PUMA-560 robot manipulator", *International Journal of Fuzzy Logic Systems (IJFLS),*ISSN : 1839-6283, December 2013.

5. **D.V.Pushpalatha**, K.R.Sudha, Swati Devabhaktuni, "PLC based Smart Street Lighting Control", International Journal of Intelligent Systems and Applications (IJISA),ISSN: 2074-9058 , DOI: 10.5815/ijisa.2014.01.08, page no. 64-72, Impact factor 3.5
6. **D.V.Pushpalatha**, Swati Devabhaktuni, "On line speed control of PMDC motor using Auto tuning PID through LabVIEW", International Journal of Electronics and Electrical Engineering (IJEEE), ISSN: 2301-380X, Vol. 1, No. 4, December, 2013, Engineering and Technology Publishing doi: 10.12720/ijeee.1.4.230-235, Impact Factor 2.2.
7. Swati Devabhaktuni**, D.V.Pushpalatha,** "Soil Moisture and Temperature sensor based intelligent irrigation water pump controlling system using PIC 16F72 Microcontroller", (IJETED), RS Publication, ISSN:2249-6149 , Issue 3, Vol. 4 , Page No.101-107, July, 2013, Impact Factor 2.87.
8. **D.V.Pushpalatha**, "Simulation of PLC based Smart Street Lighting Control using LDR", IJLTET, ISSN : 2278-621X, Issue 4, Vol.2, Page No. 113-121, July , 2013.
9. Ch.Ravi Kumar, K.R.Sudha, **D.V.Pushpa Latha**, "Modelling and control of 5DOF Robot Arm using Neuro-Fuzzy Controller", International Journal of Engineering Research & Technology (IJERT), Vol. 1 Issue 7, September 2012, ISSN: 2278-0181,Page no.1-8, Impact factor 1.76.
10. K.R.Sudha, **D.V.Pushpa Latha**, "Design of Adaptive Controller for a robot gripper using Support Vector Regression", International Journal of Computational Intelligence and Information Security, March 2011, vol.2, No.3, Page No. 50-54, Impact factor 0.583.

National Publications: 3

1. **D.V.Pushpalatha**, P.SrividyaDevi, P.M.Sarma, "Measurement of power and energy using Arduino", Research Journal of Engineering Sciences, ISSN 2278-9472, Vol. **2(10),** 10-15, October **(2013).**
2. **D.V.Pushpalatha**, K R Sudha, Swati Devabhaktuni, "Millenium3 PLC based Temperature Control using LM35", Research Journal of Engineering Sciences, ISSN 2278-9472,Vol.2(6),Page 1-7, June, 2013
3. **D.V.Pushpa Latha**, K.R.Sudha, "RBF Kernel Support Vector Regression based controller for a Robot Gripper", Journal Of Computing, Vol.3, Issue 3, March 2011, ISSN 2151-9617, Page no.107-111, Impact Factor 1.076.

Conference Proceedings: 17

National Conferences: 7

1. P.Srividyadevi, P.Sirisha, **Dr.D.V.Pushpalatha** "Design of Line following Robot using Arduino" National conference on Contemporary Control (ConCon)2014, Andhra University, Visakhapatnam, Nov, 2014.

2. **D.V.Pushpalatha**, P.Srividyadevi, R.Vijaya santhi "Speed Control of DC Motor using Arduino"National conference on Contemporary Control (ConCon)2014, Andhra University, Visakhapatnam, Nov, 2014.

3. P.Sirisha, P.Srividyadevi, **D.V.Pushpalatha** "Ultrasonic RADAR system using Arduino", National conference on Contemporary Control (ConCon)2014, Andhra University, Visakhapatnam, Nov, 2014.

4. **Dr.D.V.Pushpa Latha**, M.Rekha, Dr. K.R.Sudha, "System identification of dc motor-generator set using matlab/labview"**,** National Conference on Contemporary control and Soft computing in Electrical Engineering, Andhra University, Visakhapatnam, May, 2013.
5. Swati Devabhaktuni , **Dr.D.V.Pushpa Latha** , "Speed control of separately excited d.c motor using chopper", National Conference on Contemporary control and Soft omputing in Electrical Engineering, Andhra University, Visakhapatnam, May, 2013.
6. **Dr.D.V.Pushpa Latha** , Dr.K.R.Sudha, Swati Devabhaktuni ,"Labview Based Temperature Control Using LM 35", National Conference on Contemporary control and Soft computing in Electrical Engineering, AndhraUniversity, Visakhapatnam, May, 2013.
7. **D.V.Pushpa Latha**, K.R.Sudha, Ch.Ravi Kumar, "Modelling and control of 5 DOF Robot Arm using Neuro-Fuzzy Controller", National Conference on Contemporary control and Soft computing in Electrical Engineering, Andhra University, Visakhapatnam, 30-31, May, 2012.

International Conferences: 10

1. P. Ananthababu, Ch. V. N Raja, **D. V. P Latha**, K. R Sudha, "Design, Implementation and Speed tracking of Linear Induction Motor for Maglev Transportation System",

International Conference on Soft Computing, Intelligent Systems and Applications,Springer Advances in Intelligent Systems and Computing Series,, April 8-9, 2016, Bangalore**.**

2. **D.V.Pushpalatha**, P.SrividyaDevi, " IoT Based Security application using Raspberry Pi", International Conference on Soft Computing, Intelligent Systems and Applications,Springer Advances in Intelligent Systems and Computing Series,, April 8-9, 2016, Bangalore**.**

3. Ananth Babu, Ch.V.N.Raja, **D.V.Pushpalatha**, K.R.Sudha, "Design of Fractional Model Reference Adaptive PID Controller to Magnetic Levitation System with Permagnet", International Journal of Systems, Control and Communications, InderScience Publishers, Vol. 7, No. 1, 2016;impact factor 1.554.

4. Lakshmi Kanumuri, **D V Pushpalatha**, Swadesh Kumar Singh, "A Hybrid Neural Network - Genetic Algorithm for Prediction of Mechanical Properties of ASS-304 at Elevated Temperatures", 5th International Conference on Materials Processing and Characterization, Elsevier Publishers, Materials Today Proceedings,February 12-13, Hyderabad, 2016.

5. P.Srividyadevi,**Dr.D.V.Pushpalatha,**R.Vijaya santhi "Introducing LQR-Fuzzy with dynamic Demand Response Control loop to LFC Model", Advances in control and Optimization of Dynamical system(ACODS) with IEAC(International Federation & Automatic Control),Feb 1st to Feb 5th 2016, NIT Trichy Tamilnadu.

6. **D.V.Pushpalatha**, Padmalaya Nayak, " A Clustering Algorithm for WSN to Optimize the Network Lifetime Using Type-2 Fuzzy Logic Model ", 2015 IEEE Third International Conference on Artificial Intelligence, Modelling and Simulation, December 2-4, 2016, Malaysia.

7. **Dr.D.V.Pushpalatha**,P.Srividyadevi "Internet of things Embedded with Raspberry pi and Arduino" eISSN: 2319-1163 | pISSN: 2321-7308Volume: 04 Special Issue: 12 | NCIDCEME-2015 | Oct-2015, Available @ http://www.ijret.org.

8. Lakshmi Kanumuri**, D.V.Pushpalatha**, Amit Kumar Gupta, Swadesh Kumar Singh, "Study the Effect of Temperature on the Properties of ASS-304 Using ANN", Processing and Fabrication of Advanced Materials XXIII, PFAM, IIT Rourkela,Vol.2, pp 1186-1192, Dec 5 -7 2014.

9. **D.V.Pushpalatha**, K.R.Sudha and A.Satya Devi "Design of Adaptive Fuzzy Controller for a Robot gripper" proceedings of IEEE Advances in Recent Technologies in Communication and Computing, ARTCom '09. International Conference on 27-28 Oct. 2009 , pp 254 – 256.
10. Ch.Ravi Kumar, K.R.Sudha, **D.V.Pushpalatha**, Ch.V.N.Raja, "Fuzzy CMeans Controller for a PUMA-560-Robot manipulator", IEEE Workshop on Computational Intelligence: Theories, Applications and Future Directions, CIS 2013, IIT Kanpur, India, pp. 57-62, July 2013

Dr.J.Sridevi, PROFESSOR, Dept.EEE (ID-516)

Qualifications: Ph.D from JNTUH, Hyderabad in power systems (2014)

M.Tech (2006), **B.Tech** (Electrical and Electronics Engineering) (2002).

Experience: 15 Years

Research interest: Power Systems, FACTS

Journal Publications/Conference Proceedings: 12

International Journals

1. **Dr J.Sridevi** , Implementation Of Thyristor Controlled Series Capacitor (TCSC) In Transmission Line Model Using Arduino, Int. Journal of Engineering Research and Applications ISSN : 2248-9622, Vol. 4, Issue 9(Version 5), pp.114-117, September 2014.

2. **J.Sridevi**,"A new active power factor correction using boost converter",International science congress association research journal of engineering,ISSN:2278-9472,Vol2(8),pp-7-11,Aug,2013.

3. **J. Sridevi**, Dr. J. Amarnath, Dr. G. Govinda Rao, "Influence of FACTS Devices on Congestion Management in Deregulated PowerSystem", ICGST-ACSE Journal, Volume 11, Issue 2, November 2011.

4. **J. Sridevi**, Dr. J. Amarnath, Dr. G. Govinda Rao, "Comparison of Voltage Stability by optimal location of TCSC and SVC using Loss Sensitivity Method under Contingencies", International Review on Modelling and Simulations (I.RE.MO.S), Vol.4, N.4, October 2011.

5. **J. Sridevi**, Dr. J. Amarnath, Dr. G. Govinda Rao, "Impact of FACTS devices on Zonal Congestion Management in Deregulated Power System", Innovative Systems Design and Engineering, IISTE, Vol 3, No 1, 2012.

6. **J. Sridevi**, Dr. J. Amarnath, Dr. G. Govinda Rao, "Optimal Location of FACTS Devices for Zonal Congestion Management under Contingency", International Journal of Applied Engineering Research ISSN 0973-4562 Volume 7, Number 6 (2012) pp. 599- 611.

7. **J. Sridevi,** Dr. J. Amarnath, Dr. G. Govinda Rao, "Role of FACTS Devices on Zonal Congestion Management Ensuring Voltage Stability under Contingency", International Journal of Advances in Engineering & Technology, Vol. 3, Issue 2, pp, 635-641, May 2012.

8. **J. Sridevi**, Dr. J. Amarnath, Dr. G. Govinda Rao, " Zonal Power Quality improvement using Static Var Compensator for an Indian Utility System", International Journal of Engineering Research and Applications (IJERA) , Vol. 2, Issue 3, May-Jun 2012, pp. 1320- 1325.

9. **J. Sridevi**, Dr. J. Amarnath, Dr. G. Govinda Rao, "Congestion Management and ATC Enhancement with optimal location of FACTS devices using Sensitivity indices", International Review on Modelling and Simulations (I.RE.MO.S), Vol.4, N.4, August 2011

National Conferences

1. **J. Sridevi**, Dr. J. Amarnath, Dr. G. Govinda Rao, "Assessment of Available Transfer Capability using AC Power Transfer Distribution Factors", Proceeding of National Conference on Power System Planning , operation& optimization(PSPOO-2011), J.N.T.University, Kakinada.
2. **J. Sridevi**, M. Abhiram, Dr. J. Amarnath, Dr. G. Govinda Rao, "ATC Enhancement with Multi type FACTS devices", Proceeding of National Conference on Power System Planning , operation& optimization(PSPOO-2011), J.N.T.University, Kakinada.
3. **J. Sridevi**, Dr. J. Amarnath, Dr. G. Govinda Rao, "Zonal Congestion Management with Optimal Placement of FACTS Devices under Contingency", Proceeding of National Conference on Recent Trends in Power systems and Power Electronics(NCPSPE-2012), BVC Engineering College, Odalarevu.

Dr. S.V.JAYA RA KUMAR, PROFESSOR, Dept.EEE (ID-1291)

Qualifications: Ph.D from IIT Kanpur (2000)

M.Tech (1979),**B.Tech** (Electrical and Electronics Engineering) (1976).

Experience: 36 +Years

Research interest: Power Systems, FACTS

Journal Publications/Conference Proceedings: 26

International Journals

1. V.Vijaya Rama Raju, **Dr.S.V.Jayarama Kumar**, "An Optimal PMU Placement method for Power System Observability", 2016 IEEE Power and Energy Conference at Illinois (PECI) organized by the Power and Energy Systems Group at the University of Illinois at Urbana-Champaign, USA February 19-20, 2016.
2. Selection of capacitors for the SEIG with external rotor capacitance, in Journal of energy technologies, Feb 2012.

3. Power flow analysis of self excited induction generator, in International journal of advanced engg and research January 2012.

4. Digital relay based adaptive protection for distributed systems IEEMA journal February 2012.

5. A smart discrete fourier transform algorithm based digital multi function relay for power system protection, International journal of computer and electrical engineering February 2011.

6. Design of excitation capacitance for self excited induction generator International journal of electrical engg and electrical systems jan 2011.

7. Performance analysis of SEIG driven at variable wind speeds, International journal of engg and advanced technology Dec 2011.

8. Analysis of SEIG connected to grid interface with multi level inverter, International journal of science and technology, Dec 2011.

9. Impact of PF loop at rectifier unit on SEIG connected to grid, International journal of engineering and technology, Dec 2011.

10. Design and control of voltage regulators for SEIG, International journal of advances in engineering & technology Nov 2011..

11. Static voltage stability index for power system with UPFC,IETECH Journal of electrical analysis 2010.

12. A novel HVDC control strategy for interconnected power system a graphical based solution, American journal of scientific research 2010.

13. A modified full cycle discrete fourier transform algorithm base digital multi function relay for power transmission line protection, APRN Journal of engineering and applied sciences, August 2010.

14. Robst fuzzy load frequency controller for a two area interconnected power system, Journal of theoretical and applied information technology 2009.

15. Effect of TCSC on ill conditioned power system, The IFCAI Journal of electrical & electronics engineering, October 2009.

16. Load flow solution for ill conditioned power systems with FACTS devices, Journal of theoretical and applied information technology, June 2009.

17. Load frequency controller for a two area interconnected power system using robust genetic algorithm controller, Journal of theoretical and applied information technology, December 2008.

18. Comparative study of different control strategies for load frequency problem with emphasis on new fuzzy logic controller, International Journal of electronic an electrical engineering, November 2008.

19. Performance comparison of open and closed loop operation of UPFC, ARPN Journal of engineering and applied sciences, October 2008.

20. New control strategy for load frequency problem of a single area power system using fuzzy logic control, Journal of theoretical and applied information technology, April 2008.

21. Study of Torsional interaction with Thyristor controlled capacitor an power system stabiliser, Institutions of Engineers India Journal, March 2001 pp 175-181.

22. Damping of subsynchronous resonance oscillations with TCSC and PSS and their control interaction, Electrical power system research (ELSEIVER) 2000 pp29-36.

23. Subsynchronous resonance analysis using thyristor controlled series capacitor, Institutions of Engineers India Journal, September 2000 pp 91-96.

24. Model based control design of a TCSC compensated power system, Electrical power and Energy system (ELSEIVER) 1999 pp 299-307.

25. Subsynchronous resonance analysis using a discrete time model of thyristor controlled series compensator, Electrical power and Energy system (ELSEIVER) 1999 pp 571-578.

26. Damping of subsynchronous transient torque oscillatins using STATCOM, Institutions of Engineers India Journal, November 1999 pp 104-107.

Dr. Dola Gobinda Padhan, PROFESSOR, Dept.EEE (ID-1283)

Qualifications: Ph.D from IIT Guwahati (2012)

M.Tech (2002),**B.Tech** (Electrical and Electronics Engineering) (1999).

Experience: 15 Years

Research interest: Control Theory Applications to Power Electronics & Power Systems

Journal Publications/Conference Proceedings: 22

International Journals

1. R. Geshma Kumari and **D. G. Padhan**, "Sliding mode speed control of Permanent Magnet Synchronous Motor ", IJIREEICE, vol. 3, issue 10, pp. 16-19 October 2015 (ISSN (Online) 2321 – 2004)
2. P. Parthasaradhy, **Dola Gobinda Padhan**, Chinmaya K A," Regulation of the DC Bus Voltage of a Three Phase Active Power Filter by PI and Fuzzy Logic Controller", International Journal The IJES, ISSN (e): 2319 – 1813 ISSN (p): 2319 – 1805, pp. 78-84, 2014.

3. **D. G. Padhan** and S. Majhi, "Enhanced Cascade Control for a Class of Integrating Processes with Time Delay", ***ISA Transactions, Elsevier***, vol. 52, pp. 45-55, 2013.
4. **D. G. Padhan** and S. Majhi, "A New Control Scheme for PID Load Frequency Controller of Single-area and Multi-area Power Systems", ***ISA Transactions, Elsevier***, vol. 52, pp. 242–251, 2013.
5. **D. G. Padhan** and S. Majhi, "An Improved Parallel Cascade Control Structure for Processes with Time Delay", ***Journal of process control, Elsevier,*** vol-22, pp. 884-898, 2012.
6. **D. G. Padhan** and S. Majhi, "Modified Smith Predictor based Cascade Control of Unstable Time Delay Processes", ***ISA Transactions*****, Elsevier,** vol-51, pp. 95-104, 2012.
7. **D. G. Padhan** and S. Majhi, "Modified Smith Predictor and Controller for Time Delay Processes", ***IET Electronics letter***, vol-47, No.-17, pp. 959-961, 2011.

International Conferences

1. **D. G. Padhan** and B. Rajagopal Reddy, "A New Tuning Rule of Cascade Control Scheme for Processes with Time Delay"***in IEEE International Conference PCCCTSG-2015, Kurnool 11-12 December 2015***
2. P. Parthasaradhy, **Dola Gobinda Padhan**, Chinmaya K A," Regulation of the DC Bus Voltage of a Three Phase Active Power Filter by PI and Fuzzy Logic Controller", ***International Conference ICIEEE-2014 (Proceedings ISBN No:9789382163558), Guru Nanak Institutions Technical Campus, Hyderabad, 05-06 September 2014.***
3. **D. G. Padhan**, "A Dead Time Compensator for Integrating & Unstable Processes" ***in International Conference ICIECE-2013, 9-10 August 2013.***
4. **D. G. Padhan** and S. Majhi, "Synthesis of PID tuning for a new parallel cascade control structure" in ***IFAC Conference on Advances in PID Control PID'12, , Brescia, Italy, 28-30 March 2012.***
5. **D. G. Padhan** and S. Majhi, "Improved Parallel Cascade Control Structure for Time Delay Processes" ***IEEE INDICON 2011, BITs Pilani Hydrabad Campus, 16-18 December 2011.***
6. **D. G. Padhan** and S. Majhi, "A Two-degree-of-freedom Control Scheme for Improved Performance of Unstable Delay Processes" ***IEEE ICECE 2010, Dhaka, Bangladesh, 18-20 December 2010.***
7. **D. G. Padhan** and S. Majhi, "A Two-degree-of-freedom Control Scheme for Integrating and Unstable Delay Processes" ***IEEE INDICON 2010, Jadavpur University, Kolkata, 17-19 December 2010.***
8. **D. G. Padhan** and S. Majhi, "Modified Smith Predictor and Controller for Stable and Unstable Processes "***4th International Conference On Computer Applications in Electrical Engineering Recent Advances, IIT Roorkee, 19-21 February 2010.***
9. **D. G. Padhan** , "Transient stability and power flow models of FACTS controllers" **International Conference PCO-2008, Chiang Mai, Thailand, 18 – 20 July 2008.**
10. **D. G. Padhan** , "Optimal Bidding Strategies for Steam Turbine Generators" ***International Conference PSACO-2008, College of Engineering, Andhra University, Visakhapatnam, 13-15 March 2008.***

National Conferences

1. **D. G. Padhan** and S. Majhi, "An Improved Cascade Control Structure for Time Delay Processes" ***35th national systems conference NSC-2011 , IIT Bhubaneswar, 9-11 December, 2011.***
2. **D. G. Padhan** and S. Majhi, "A Two-degree-of-freedom Control Scheme for Improved Performance of Integrating Delay Processes" ***34th national systems conference NSC-2010 , NITK Surathkal, 10-12 December, 2010.***
3. **D. G. Padhan** and S. Majhi, "Modified Smith Predictor and Controller Based on GM(1,1) Model" ***National Conference on Electronic Technologies , NCET-2010, GEC, Goa, 16-17 April, 2010.***
4. **D. G. Padhan** , "Deregulation and Regulatory Reform in the Electric Power Sector : A case study in Restructuring" ***National Conference, The Institution of Engineers (India), Pune, 23 -24 November 2007.***
5. **D. G. Padhan** , "Recent Advances in Electrical Engineering" ***National Conference, Vignan's Institute of Information Technology, Visakhapatnam, 9-10 March 2007.***

Mr.V.Vijaya Rama Raju, ASSOCIATE PROFESSOR, Dept.EEE(ID-361)

Qualifications:Ph.D(Pursuing) from JNTU

M.Tech (2001), **B.Tech** (Electrical and Electronics Engineering) (1998).

Experience: 17yrs

Research interest: Power systems

Journal Publications/Conference Proceedings: 11

International Conferences

1. **V.Vijaya Rama Raju**, Dr.S.V.Jayarama Kumar, "An Optimal PMU Placement method for Power System Observability", 2016 IEEE Power and Energy Conference at Illinois (PECI) organized by the Power and Energy Systems Group at the University of Illinois at Urbana-Champaign, USA February 19-20, 2016.
2. **V.Vijaya Rama Raju**, Divya Mereddy, "Smart Dual Axes Solar Tracking System", 2015 IEEE International Conference on Energy Systems and Applications (ICESA 2015) organized by Dr. D. Y. Patil Institute of Engineering and Technology, Pune, India 30 Oct - 01 Nov, 2015 sponsored by IEEE Pune section.
3. Dr.M.Chakravarthy,Srinivasa R Nookala, **V.Vijaya Rama Raju,** P.M.Sarma, "A Report on erection of Grid tied 100kW Solar Power Plant", International Conference On Electromagnetic Interference & Compatibility (INCEMIC 2015) organized

by Andhra University, Visakhapatnam, in association with Society of EMC Engineers(India), 22 - 23 July, 2015.

National Conferences

1. Vinay Kumar.A, P M Sarma, **V Vijaya Rama Raju** "Identification of Multi Input – Multi Output Control System Using MATLAB & LABVIEW", VIMANTRA 2013 – The National Technical Paper Contest for Research Scholars organized by National Instruments India, September 2, 2013.
2. **V.Vijaya Rama Raju,** M.Chakravarthy, P.M.Sarma, and Dr.S.N.Saxena "Reactive Power Compensation of Grid using DFIG", Proc. of National Conference on Special Electrical Machines &Systems, 16-17 February, 2010, BHEL Corporate R&D Division, Hyderabad, A.P.,India.
3. M.Chakravarthy**, V.Vijaya Rama Raju,** P.M.Sarma, and Dr.S.N.Saxena "Automation of Testing Procedure for Electrical Machines Using PLC", Proc. of National Conference on Special Electrical Machines & Systems, 16-17 February, 2010, BHEL Corporate R&D Division, Hyderabad, A.P.,India.
4. Sarma P M, **Vijayaramaraju V,** Chakravarthy M, Saxena S N, "Closed Loop Speed Control of a DC Motor Using Lab view", Design, Operation & Control of Distribution Systems, Sreenidhi Institute of Science & Technology, 2009, 32-38

5. Sarma P M, **Vijayaramaraju.**V, Chakravarthy M, Saxena S N, "Microprocessor Based Industrial Timer", Design, Operation & Control of Distribution Systems, Sreenidhi Institute of Science & Technology, 2009, 39-42
6. S.S. Nawaz, R.Anil Kumar, R.Anil Kumar, **V.Vijaya Rama Raju** and P.M.Sarma,"Reactive PowerCompensation of Grid using DFIG," Proc. of National Conference on Advanced Controls, 26-27 November2009, Gokaraju Rangaraju Inst. of Engg. and Tech., Hyderabad, India.
7. **V.Vijaya Rama Raju,** M.Chakravarthy, P.M.Sarma, and Dr.S.N.Saxena "Automation of Electrical machine testing using PLC," Proc. of National Conference on Advanced Controls, 26-27 November 2009, Gokaraju Rangaraju Inst. of Engg. and Tech., Hyderabad, India.
8. **V.Vijaya Rama Raju,** M.Chakravarthy, P.M.Sarma, and Dr.S.N.Saxena "Automatic Power Factor Correction using PLC," Proc. of National Conference on Advanced Controls, 26-27 November 2009, Gokaraju Rangaraju Inst. of Engg. and Tech.,Hyderabad, India**.**

A.Vinay Kumar, ASSOCIATE PROFESSOR, Dept.EEE (ID-881)

Qualifications: Ph.d(pursuing) from JNTUH

M.Tech (2003) Gitam University, **B.Tech** (Electrical and Electronics Engineering) (2007).

Experience: 9yrs

Research interest: Power Systems

Journal Publications/Conference Proceedings: 13

International Conferences

1. **A.Vinaykumar** ,Dr.J.Praveen ,Dr.STaraKalyani "Various power Quality issues :Measurement of Flicker and Mitigation of Voltage sag" An International Conference on Intelligent and Efficient Electrical Systems conducted at PSG college of technology Coimbatore, 12-14 Dec 2013
2. **Vinay Kumar A,** Dr. J.Praveen," Digital Simulation of Dynamic Voltage Restorer using Direct Power Control Strategy", presented at 2nd International Conference on Nanotechnology and Biosensors(ICNB-2,2011) on 27th and 28th December, 2011 organized by IACQER at Raghu Engineering College.

National Conferences

1. P.Sharat Chandra, J.Praveen, **A.Vinay Kumar**, " Design and Implementation of Three Phase Inverter Fed Induction Motor", published in the proceedings of National Conference on Innovations and Design Challenges in Electrical and Medical Electronics, 21-22nd , Aug,2015, held at GRIET, Hyderabad. Pp 92-96. ISBN:978-1-944541-82-8.
2. L.Sravan Kumar, **A.Vinay Kumar**, J.Praveen," Mitigation of Voltage Sag to Improve Power Quality by Dynamic Voltage Restorer", published in the proceedings of National Conference on Innovations and Design Challenges in Electrical and Medical Electronics, 21-22nd , Aug,2015, held at GRIET, Hyderabad. Pp 97-103.ISBN:978-1-944541-82-8.
3. **Vinay Kumar. A**, Dr. Pradeep Nirgude, D Devendranath "Implementation of FIR Filters in Reduction of Noise and Smoothening of Data for Evaluation of Lightning Impulses", presented at National High Voltage Engineering Conference (NHVEC-2014), 7th – 8th March 2014, CPRI-Hyderabad, page: 26 – 30.
4. **Vinaykumar.A,** Dr.J.Praveen, Dr.S.Tarakalyani "Measurement of Voltage Filcker and Mitigation of Voltage Sag to improvise the power Quality" presented at National Conference on "Power Quality and Industrial Drives" conducted by Sreenidhi Institute of Science and Technology ,Hyderabad,July12-13th 2013
5. **A Vinay Kumar,** Dr J Praveen, Dr S Tara Kalyani, "Adoption of Direct Power Control Strategy for Enhancement of Dynamic Voltage Restorer" presented at a National Conference on Electrical Power Engineering and Industrial Drives during May 18-19,2012 Organized by Sreenidhi Institute of Science and Technology (Autonomous), Hyderabad.
6. Dr J Praveen , **A Vinay Kumar**," Adoption of Direct Power Control Strategy And Dynamic Voltage Restorer for Improvement of Power Quality" presented at IETE sponsored National Conference on Emerging Trends in Engineering & Applications (NCETETA-2010) to be held on 12th & 13th November, 2010 at Shirdi Sai Engineering College, Bengaluru.

National Journal

1. **Vinay Kumar A,** J.Praveen, S.Tarakalyani "Various Power Quality Issues: Measurement of Flicker and Mitigation of Voltage Sag", published in the National Journal of Technology, Vol.10, No.3, September, 2014, pp 49 -55. ISSN 0973-1334.
2. **A Vinay Kumar,** Dr J Praveen, Dr S Tara Kalyani, "Adoption of Direct Power Control Strategy for Enhancement of D ynamic Voltage Restorer", published in National Journal on Electrical Power Engineering and Industrial Drives (NJEPEID) Volume-1,Number-2, July-Aug,2012. ISSN 2231-590X page no's 200-205.

International Journal

1. **Vinay Kumar Awaar**, Praveen Jugge, Tara Kalyani S, "Field Test of Cost Effective Voltage Source Inverter For Driving An Induction Motor",published in **IEEE Explorer Digital Library,** IEEE INDICON 2015 1570203957, 978-1-4673-6540-6/15/$31.00 ©2015 IEEE.
2. **Vinay Kumar. A,** Dr. J Praveen, Dr. S Tarakalyani "PQ Improvement By Moderation of Multi-Level Inverter Controlling Techniques And Intensifying The Performance of DVR" published in International Journal of Advances in Electrical and Electronic Engineering (AEEE) journal in the month of June 2015 with the **Vol.13 No.2 (2015)** page no.s from 107-114.
3. Dr J Praveen, **A Vinay Kumar**, " Power Quality Improvement with Dynamic Voltage Restorer using Direct Power Control Strategies" published in International Journal of Electrical, Electronics and Computing Technology pp 20-25 Volume -1, Issue-2, January-April 2011. ISSN 2229-3027.

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(ID-695)

Qualifications: Ph.d(pursuing) from JNTU

M.Tech (2010), **B.Tech** (Electrical and Electronics Engineering) (2007).

Experience: 9 years

Research interest: Power Electronics

Journal Publications/Conference Proceedings: 5

International Journals:2

1. S.Radhika,**Syed Sarfarz Nawaz**, "Design and Analysis of Current Mode control of Boost Converter", International Journal of Science and Research(IJSR), ISSN(Online):2319-7064,Paper ID:NOV152882,Volume 5,Issue 1,January 2016

2. S.Radhika,**Syed Sarfarz Nawaz**,M.N.Sandhya Rani, "Differential current protection of Transformer using Arduino with voice alert", International Journal of Innovations in engineering and Technology(IJIET), ISSN:2319-1058,Volume 6,Issue2,December 2015.

International Conference Proceedings:1

1. Rohan Devara, **Syed Sarfarz Nawaz** "Design of Shunt Active Power Filter for Harmonic Compensation in Power Systems" **International Conference on Innovations in Electrical & Electronics Engineering (ICIEEE-2014),** ISBN: 978-93-82163-55-8, September-2014, pp-353-358.

National Conference Proceedings:2

1. Rohan Devara, **Syed Sarfarz Nawaz** "Design of Shunt Active Power Filter for Harmonic Compensation in Power Systems" **National Conference on Electrical Sciences- (NCES-2014),** ISBN: 978-93-83083-68-8, July 2014, pp-70-75.
2. **S.S. Nawaz,** R.Anil Kumar, R.Anil Kumar, V.Vijaya Rama Raju and P. M. Sarma," Reactive Power Compensation of Grid using DFIG," Proc. of National Conference on Advanced Controls, 26-27 November2009, Gokaraju Rangaraju Inst. of Engg. and Tech., Hyderabad, India.

P.SRIVIDYA DEVI, ASSISTANT PROFESSOR, Dept.EEE (ID-931)

Qualifications: Ph. d(pursuing) from AU.

M.Tech (2008) JNTUCEH B.Tech (Electrical and Electronics Engineering) (2005).

Experience: 8yrs

Research interest: Power Systems

Journal Publications/Conference Proceedings: 9

National Journal Publications:2

1. **Srividyadevi P**, Pusphalatha D.V. and Sharma P.M. "Measurement of Power and Energy Using Arduino" Research Journal of Engineering Sciences ICSA ISSN 2278 – 9472, Vol. 2(10), 10-15, October (2013).
2. **Srividyadevi P** , G.Sreenivas " Novel Implementation Of Pspice Modeling For Reliability Analysis Of Distribution Systems" Vol 6...No 2 ..In Sept 2010 Journal Pp 167-176.

National Conferences

1. **P.Srividyadevi**,P.Sirisha,Dr.D.V.Pushpalatha"Design of Line following Robot using Arduino" National conference on Contemporary Control (ConCon),Andhra University Vishakapatanam,Nov 2014.

2. Dr.D.V.Pushpalatha,**P.Srividyadevi**,R.Vijaya santhi"Speed Control of DC Motor using Arduino"National conference on Contemporary Control (ConCon)),Andhra University Vishakapatanam,Nov 2014.

3. P.Sirisha,**P.Srividyadevi**,Dr.D.V.Pushpalatha"Ultrasonic RADAR system using Arduino"National conference on Contemporary Control (ConCon)),Andhra University Vishakapatanam,Nov 2014.

International Conferences

1. D.V.Pushpalatha, **P.SrividyaDevi**, " IoT Based Security application using Raspberry Pi", International Conference on Soft Computing, Intelligent Systems and Applications,Springer Advances in Intelligent Systems and Computing Series,, April 8-9, 2016, Bangalore**.**

2. **P.Srividyadevi,**Dr.D.V.Pushpalatha**,**R.Vijaya santhi "Introducing LQR-Fuzzy with dynamic Demand Response Control loop to LFC Model", Advances in control and Optimization of Dynamical system,Feb 1st to Feb 5th 2016, NIT Trichy Tamilnadu.

3. **P.Srividyadevi,**Dr.D.V.Pushpalatha**,**R.Vijaya santhi "Introducing LQR-Fuzzy with dynamic Demand Response Control loop to LFC Model", Advances in control and Optimization of Dynamical system,Feb 1st to Feb 5th 2016, NIT Trichy Tamilnadu.

4. Dr.D.V.Pushpalatha**,P.Srividyadevi** "Internet of things Embedded with Raspberry pi and Arduino" eISSN: 2319-1163 | pISSN: 2321-7308Volume: 04 Special Issue: 12 | NCIDCEME-2015 | Oct-2015, Available @ http://www.ijret.org.

M.SRIKANTH, ASSISTANT PROFESSOR, Dept.EEE (ID-608)

Qualifications:

M.Tech (2008), **B. Tech** (Electrical and Electronics Engineering) (2005).

Experience: 9 years

Research interest: Power Electronics

Journal Publications/Conference Proceedings: 1

National Conference Proceedings:1

1. R.Pavan Kumar, **M.Srikanth**, "Cascaded H-Bridge Multi Level Inverter Using Selective Harmonic Elimination Technique", National Conference On Electrical Sciences 2014(NCES-2014), ISBN:978-93-83083-68-8, July 2013.

P.PRAVEEN KUMAR, ASSISTANT PROFESSOR, Dept.EEE (ID-609)

Qualifications:

M.Tech (2008), **B. Tech** (Electrical and Electronics Engineering) (2006).

Experience: 8 years

Research interest: Power Electronics & Power Systems

Journal Publications/Conference Proceedings: 1

National Conference Proceedings: 1

1. **P.Praveen Kumar,** "Analysis of new three-phase 9-level multilevel inverter with reduced number of power electronic components", International Journal of Professional Engineering Studies, ISSN print: 2374-3751 ISSN e: 2374-3761,volume-V,Issue-3,Aug-2015, Impact Factor :2.825.

R.Anil Kumar, ASSISTANT PROFESSOR, Dept.EEE (ID-657)

Qualifications: M.Tech (2010), **B.Tech** (Electrical and Electronics Engineering) (2006).

Experience: 8yrs

Research interest: Micro Controllers

National Conference Proceedings:2

1. A. V. Subbarao, **R. Anil Kumar, "**voltage control of pmbldc motor drive using pfc cuk converter", National Conference on Innovations & Design Challenges in Electrical & Medical Electronics, with IJRET: International Journal of Research in Engineering and Technology, eISSN: 2319-1163 | pISSN: 2321-7308Volume: 04 Special Issue: 12 | NCIDCEME-2015 | Oct-2015, Available @ http://www.ijret.org.

2. S.S. Nawaz, **R.Anil Kumar**, V.Vijaya Rama Raju and P.M.Sarma,"Reactive Power Compensation of Grid using DFIG," Proc. of National Conference on Advanced Controls, 26-27 November2009, Gokaraju Rangaraju Inst. of Engg. and Tech., Hyderabad, India.

U.Vijaya Laxmi, ASSISTANT PROFESSOR, Dept.EEE (ID-692)

Qualifications: M.Tech (2010), **B.Tech** (Electrical and Electronics Engineering) (2007).

Experience: 8yrs

Research interest: Electrical Measurements

National Conference Proceedings:2

5. **U.Vijaya Lakshmi,** M.Chakravarthy, Dr.S.N.Saxena, P.M.Sarma "Investigation of various PWM Techniques for Shunt active Power Filter"NADCON-2009, Nov 26-27,2009,GRIET,Hyderabad.

6. **U,Vijaya Lakshmi,E.**Satish Reddy, "step up dc – dc converter using active clamp flyback, boost converter and vdr with control strategies", eISSN: 2319-1163 | pISSN: 2321-7308Volume: 04 Special Issue: 12 | NCIDCEME-2015 | Oct-2015, Available @ http://www.ijret.org.

G. Swapna, ASSISTANT PROFESSOR, Dept.EEE (ID-746)

Qualifications: M.Tech (2013), **B.Tech** (Electrical and Electronics Engineering) (2008).

Experience: 6yrs

Research interest: Power Electronics

International Journal Publication:1

1. **G.Swapna**, V.Himabindu,P.M.Sarma,M.Chakravarthy "Hardware implementation of single phase Inverter" International journal of engineering trends and technology, Volume 4,Issue 8,August 2013, ISSN2231-5381.

V. Hima Bindu, ASSISTANT PROFESSOR, Dept.EEE (ID-772)

Qualifications: M.Tech (2013), **B.Tech** (Electrical and Electronics Engineering) (2000).

Experience: 6yrs

Research interest: Power Electronics

International Journal Publication:1

1. G.Swapna, **V.Himabindu**,P.M.Sarma,M.Chakravarthy "Hardware implementation of single phase Inverter" International journal of engineering trends and technology, Volume 4,Issue 8,August 2013, ISSN2231-5381.

S.RADHIKA, ASSISTANT PROFESSOR, Dept.EEE (ID-863)

Qualifications: M.Tech (2011), **B.Tech** (Electrical and Electronics Engineering) (2009).

Experience: 6yrs

Research interest: Power Electronics

International Journal Publication: 3

1. **S.Radhika,**Syed Sarfarz Nawaz, "Design and Analysis of Current Mode control of Boost Converter", International Journal of Science and Research(IJSR), ISSN(Online):2319-7064,Paper ID:NOV152882,Volume 5,Issue 1,January 2016.
2. **S.Radhika,**Syed Sarfarz Nawaz,M.N.Sandhya Rani, "Differential current protection of Transformer using Arduino with voice alert", International Journal of Innovations in engineering and Technology(IJIET), ISSN:2319-1058,Volume 6,Issue2,December 2015.
3. S.Swathi, **S.Radhika, "**Development and Testing of Non-Isolated Flyback Converter",International Journal of Scientific Engineering & Technology Research(IJSTER), ISSN 2319-8885 Vol.05,Issue.01, January-2016, Pages:0111-0121**.**

V.V.S. Madhuri, ASSISTANT PROFESSOR, Dept.EEE (ID-879)

Qualifications: MTech (2007), **B.Tech** (Electrical and Electronics Engineering) (2002).

Experience: 9yrs

Research interest: Power systems

International Journal Publication:2

1. **V.V.S.Madhuri**, P.Mallikarjuna Sarma, M.N.SandhyaRani, "Automatic Street Lighting using PLC", International Journal of Latest Trends in Engineering and

Technology (IJLTET), ISSN: 2278-621X, PP no.353 to 357, Vol. 2, Issue 4, July 2013.

2. **V.V.S.Madhuri**,P.Mallikarjuna Sarma,M.Chakravarthy "Automatic Solar tracking using Crouzet Millenium PLC" International journal of latest trends in engineering and technology,Vol.2,Issue 4, July 2013, ISSN 2278-621X.

Ms. M.N.Sandhya Rani, ASSISTANT PROFESSOR, Dept.EEE (ID-882)

Qualifications: M.Tech (Power Electronics)(2015), **B.Tech** (Electrical and Electronics Engineering) (2011).

Experience: 5yrs

Research interest: Power Electronics

International Journal Publication:4

1. S.Radhika,Syed Sarfarz Nawaz**,M.N.Sandhya Rani**, "Differential current protection of Transformer using Arduino with voice alert", International Journal of Innovations in engineering and Technology(IJIET), ISSN:2319-1058,Volume 6,Issue2,December 2015.
2. **M.N.Sandhya Rani,** "A Novel Method to Improve the Dynamic Response of a Single Phase PWM Rectifier by Implementing Digital Filter", International Journal of Scientific Engineering and Technology Research(IJSETR), ISSN 2319-8885, Vol.04, Issue.33, August-2015, Pages:6626-6633, Impact Factor :3.162.
3. V.S.Madhuri, P.Mallikarjuna Sarma**, M.N.SandhyaRani**, "Automatic Street Lighting using PLC", International Journal of Latest Trends in Engineering and Technology (IJLTET), ISSN: 2278-621X, PP no.353 to 357, Vol. 2, Issue 4, July 2013.
4. D. Anusha, PM.Sarma, **M.N. SandhyaRani**, "Appliance Remote Control Using Arduino" International Journal of Latest Trends in Engineering and Technology (IJLTET), ISSN: 2278-621X, Page no. 35 to 41, Vol. 2 , Issue 4 , July 2013.

G. Sandhya Rani, ASSISTANT PROFESSOR, Dept.EEE (ID-888)

Qualifications: M.Tech (2011), **B.Tech** (Electrical and Electronics Engineering) (2005).

Experience: 5yrs

Research interest: Power Electronics

National Journal Publication:1

1. **G. Sandhyarani**, K. Anuradha "Spectral Analysis of inverter Output Voltage Waveform Using Different PWM Techniques", National Journal on Electrical Power Engineering and industrial Drives ISSN 2231-590X, Vol.1. No.2, 2012 (Pg 186-191).

B.VASANTH REDDY, ASSISTANT PROFESSOR, Dept.EEE
(ID-930)

Qualifications: M.Tech (2011), **B.Tech** (Electrical and Electronics Engineering) (2008).

Experience: 5yrs

Research interest: Power Electronics

Journal Publications/Conference Proceedings: 9

International Journals

1. R.Arun Sagar, **B.Vasanth Reddy**, "comparative analysis of SPWM and APWM Techniques for three phase AC Chopper. International journal of emerging trends in engineering and development, issue 2, vol.4 (may-2012).
2. Ch.Nagarjuna Reddy, **B.Vasanth Reddy**, B.Chitti Babu, "performance analysis of different current control techniques for VSI connected to R-L Load. International journal on emerging trends in engineering and development ISSN: 2249-6149, issue 2, vol.5(july 2012).
3. B.Chitti Babu, **B.Vasantha Reddy**, K.B.Mohanty,"A Novel Delta Modulator and Modified Ramp Type Current Controller-Two Viable Scheme for Current Controlled Voltage Source Inverter" international journal of computer applications, Issn- 09758887, Year 2010, vol: 1, issue: 3, pages: 48-54.
4. M.Sunil Kumar, **B.Vasanth Reddy** "Comparative Analysis Hysteresis and Ramp Type Current Controllers for Grid Connected VSI" International Journal of Instrumentation, Control and Automation (IJICA), ISSN: 2231-1890.Vol -1 Issue 2.

International Conferences

1. **B.Vasantha Reddy**, B.Chitti Babu "Hysteresis controller and delta modulator- A two viable scheme for current controlled voltage source inverter" International conference for technical postgraduates (TECHPOS-2009), 14 December 2009.
2. **B.Vasantha Reddy**, B.Chitti Babu "A Decoupled Control of Grid Connected Voltage Source Inverter for Wind Power Generating Systems" 5th international conference on power electronics and machine drives (PEMD-2010), 19 April 2010.

National conferences

1. **B.Vasantha Reddy**, B.Chitti Babu, K.B.Mohanty, A.K.Panda, "Synchronous Virtual Grid Flux Oriented Control of Grid Side Converter for Distributed Power Generation System" 4th National Power Electronics Conference, NPEC-2010, 10- 13 June 2010, IIT Roorkee.
2. B.Chitti Babu, **B.Vasantha Reddy**, "An Improved Dynamic Response of Voltage Source Inverter using Novel Hysteresis Dead Band Current Controller" 18th Annual Symposium on "Emerging Needs in Computing, Communication, Signals and Power" IEEE Bangalore. 2009.
3. **B.Vasanth Reddy**, B.Chitti Babu, "Analysis of different current controlling techniques for VSI". AICTE sponsored Emerging Technological Trends National Level Conference, NCETT-2010.

BOOKS PUBLISHED:

1. **B.Vasanth Reddy**, B.Chitti Babu, "Electrical Generation and Distribution Systemsand Power Quality Disturbances" chapter entitled Power Quality Improvement by Using Synchronous Virtual Grid Flux Oriented Control of Grid Side Converter, ISBN 978-953-307-329-3.Link:http://www.intechopen.com/articles/show/title/power-quality improvement-byusing- synchronous-virtual-grid-flux-oriented-control-of-grid-side-conve

M.REKHA, ASSISTANT PROFESSOR, Dept.EEE (ID-933)

Qualifications: M.Tech (2012), **B.Tech** (Electrical and Electronics Engineering) (2004).

Experience: 3yrs

Research interest: Power Electronics

Journal Publications/Conference Proceedings: 2

International Journal: 1

1. **M.Rekha**, Mr.K.P.Swaroop "An Electromagnetic Vibrational Energy Harvesting Using Boost and Buck-Boost Converter" Published in international journal of engineering research & technology (IJERT), Vol.1 Issue 7, September-2012.

National conference:1

1. Dr.D.V.Pushpa Latha, **M.Rekha**, Dr. K.R.Sudha, "System identification of dc motor-generator set using matlab/labview"**,** National Conference on Contemporary control and Soft computing in Electrical Engineering, Andhra University, Visakhapatnam, May, 2013.

P.SIRISHA, ASSISTANT PROFESSOR, Dept.EEE (ID-934)

Qualifications: M.Tech (2012) JNTUCEH **B.Tech** (Electrical and Electronics Engineering) (2007).

Experience: 8yrs

Research interest: Power Systems

Journal Publications/Conference Proceedings: 2

National Conferences

1. P.Srividyadevi,**P.Sirisha**,Dr.D.V.Pushpalatha"Design of Line following Robot using Arduino" National conference on Contemporary Control (ConCon),Andhra University Vishakapatanam,Nov 2014.

2. **P.Sirisha**, P.Srividyadevi, Dr.D.V.Pushpalatha"Ultrasonic RADAR system using Arduino"National conference on Contemporary Control (ConCon),Andhra University Vishakapatanam,Nov 2014.

.USHA RANI, ASSISTANT PROFESSOR, Dept.EEE (ID-1045)

Qualifications: M.Tech (2009), **B.Tech** (Electrical and Electronics Engineering) (2007).

Experience: 7.8 years

Research interest: Power Systems

International Conference Proceedings: 1

1. Kuldip Singh, MadhuSmita shial,**V.Usha** , **"**Real time web based monitoring system for CO2**"** International conference in innovation in electronics and communication Engg (ICIECE) on 20-21,July 2012,GNIT- Hyderabad

P.PRASANTH KUMAR, ASSISTANT PROFESSOR, Dept.EEE

(ID-1055)

Qualifications: M.Tech (2011), **B.Tech** (Electrical and Electronics Engineering) (2008).

Experience: 3yrs

Research interest: Power Electronics

International Journal Publication: 1

1 Shiva Kumar, **P. Prashanth Kumar**, "Development and Testing of Non-Isolated Boost Converter" International Journal of Engineering Research and Technology (IJERT), ISSN: 228-0181,Vol. 2 , Issue 8 , August- 2013.

P.SARASWATHI, ASSISTANT PROFESSOR, Dept.EEE (ID-1104)

Qualifications: M.Tech (2012), **B.Tech** (Electrical and Electronics Engineering) (2010).

Experience: 2.5 years

Research interest: Power Electronics & Power Systems

International Journal Proceedings: 1

1. **P Saraswathi,** Karunakumar Davala, M Karthika, **"**New Configurations for Hybrid Filters to Improve Power Quality", IJRET: International Journal of Research in Engineering and Technology, eISSN: 2319-1163 | pISSN: 2321-7308Volume: 04 Special Issue: 12 | NCIDCEME-2015 | Oct-2015, Available @ http://www.ijret.org.

International Conference Proceedings: 1

1. **P Saraswathi,** "New low cost passive filter configuration for mitigating bus voltage distortions in distribution systems", IEEE ICBEST-2015, 978-1-5090-0160-6/15,IEEE ICBESTAug 31st september 1st-2105,Singapore.

D.KARUNA KUMAR, ASSISTANT PROFESSOR, Dept.EEE (ID-760)

Qualifications: M.Tech (2012), **B.Tech** (Electrical and Electronics Engineering) (2009).

Experience: 6 years

Research interest: Control systems & Power Systems

International Journal Proceedings: 1

1. P Saraswathi**, Karunakumar Davala,** M Karthika**,** "New Configurations for Hybrid Filters to Improve Power Quality", IJRET: International Journal of Research in Engineering and Technology, eISSN: 2319-1163 | pISSN: 2321-7308Volume: 04 Special Issue: 12 | NCIDCEME-2015 | Oct-2015, Available @ http://www.ijret.org.

K.SUDHA, ASSISTANT PROFESSOR, Dept.EEE (ID-1211)

Qualifications: M.E (2008), B.E (Electrical and Electronics Engineering) (2006).

Experience: 6.8 years

Research interest: Power Electronics and Power Systems

International Conference Proceedings: 1

1. B. Navya Sree, **K.Sudha** U. Madhuri "Fault Current Limiter in Single Phase and Three Phase Lines for Compensating Voltage Sag " International conference on Recent Trends in Engineering an Science and Management (ICRTESM) 15 March 2015 ,Paper ID: ICRTESM/JNU/2015 Page No: 871-874Jawaharlal Nehru University, New Delhi

M.KARTHIKA, ASSISTANT PROFESSOR, Dept.EEE (ID-1237)

Qualifications: M.Tech (2011), **B.Tech** (Electrical and Electronics Engineering) (2008).

Experience: 4 years

Research interest: Power Electronics and Power Quality

Journal Publications/Conference Proceedings: 7

International Journal Proceedings: 5

1. P Saraswathi, Karunakumar Davala, **M Karthika**, **"New Configurations for Hybrid Filters to Improve Power Quality"**, IJRET: International Journal of Research in Engineering and Technology, eISSN: 2319-1163 | pISSN: 2321-7308Volume: 04 Special Issue: 12 | NCIDCEME-2015 | Oct-2015, Available @ http://www.ijret.org.
2. T. Chiranjeeevi, I. V. V. Vijetha, B. N. CH. V. Chakravarthi, and **M. Karthika "Tuning and Control of Multi Variable Systems"** in International Journal of Electronics and Electrical Engineering Vol. 2, No. 4,PP.309-320 December, 2014, **doi**:**10.12720**
3. **Karthika Mangamuri** ,V V Vijetha Inti **"Comparative Analysis Of Multipulse Ac-Dc Converters In VCIMD" in** International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering Vol. 2, Issue 9, September 2013**,** ISSN (Print) : 2320 – 3765 ISSN (Online): 2278 – 8875, **Impact Factor :1.686**
4. V V Vijetha Inti ,**Karthika Mangamuri**, Ch Phani Kumar on "**A New Topology for Power Factor Correction using Resonant Converters"** in International Journal of Soft Computing and Engineering ISSN: 2231-2307, Volume-3, Issue-4, September 2013 ISSN: 2231-2307, **Impact Factor :1.224**
5. Published a Research paper entitled "**An Improved Power Quality Using AC-DC Multi-Pulse Converters in Vector Controlled Induction Motor Drives**" in the online journal ELIXIR, Karthika et al./ Elixir Elec. Engg. 38 (2011) 4383-4386.

International Conference Proceedings: 1

1. **M.Karthika**, Mr.K.P.Swaroop "**An Improved Power Quality Using AC-DC Multi-Pulse Converters in VCIMD**", in the Proceedings of International Conference on Nanoscience & Advanced Computing (ICNEAC-2011) organized by **Swarnandhra College of Engineering & Technology** on 8th -10th July -2011.

N**ational Conference Proceedings:** 1

1. **Karthika Mangamuri** ,V V Vijetha Inti "**Stability Enhancement of Single Machine Connected to Infinite-Bus with PSS"** in national level Conference NCIET 2013 ISBN: 978-1-4276-5484 organized by **Gudlavalleru Engineering College** on 27th -28th Dec -2013.